

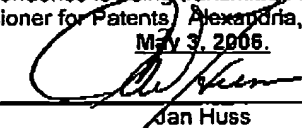
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From: Jon Y. Ikegami 650.833.2104	Client-Matter Number: 351991-991320 (2102475)	
Re: U.S. Patent Application No. 10/769,146 Filing Date: January 29, 2004 Title: SEMICONDUCTOR INTEGRATED CIRCUIT Inventor: Junichiro WATANABE		
Pages: - <u>11</u> - (including this form) Originals: will not follow		
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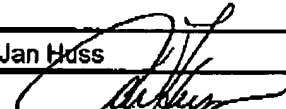
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TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/769,146	
	Filing Date	January 29, 2004	
	First Named Inventor	Junichiro WATANABE	
	Art Unit	2813	
	Examiner Name	Tuan H. Nguyen	
Total Number of Pages In This Submission	10	Attorney Docket Number	351991-991320 (2102475)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm Name	DLA Piper Rudnick Gray Cary US LLP	
Signature		
Printed name	Jan Y. Ikegami	
Date	May 3, 2006	Reg. No. 51,115

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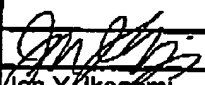
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Firm Name	DLA Piper Rudnick Gray Cary US LLP		
Signature			
Printed name	John Y. Ikegami		
Date	May 3, 2006	Reg. No.	51,115

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Jan Huss

Appl. No. : 10/769,146 Confirmation No. 8510
Applicant : Junichiro WATANABE
Filed : January 29, 2004
TC/A.U. : 2813
Examiner : Tuan H. Nguyen

Docket No. : 351991-991320 (Formerly
2102475-991320)
Customer : 26379
No.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT

RESPONSE TO OFFICE ACTION

M/S: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request the Examiner to consider the following Response in reply to the Office Action, mailed on February 3, 2006, for the above-referenced application.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

PAN10456393.1
351991- 991320]